|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Micro operation | C1 | C2 | C3 | C4 | C5 | C6 | C7 | Data2 |
| Load x in rf[0] | Xxxx | Xxxx | 0000 | 1 | 1 | 0 | Xxx | 11111010 |
| Load y in rf1 | Xxxx | Xxxx | 0001 | 1 | 1 | 0 | Xxx | 00110010 |
| Load z in rf2 | Xxxx | Xxxx | 0010 | 1 | 1 | 0 | Xxx | 00011110 |
| Rf3 =rf0 xor rf1 | 0000 | 0001 | 0011 | 1 | 0 | 0 | 111 | xxxxxxxx |
| Rf4 = not rf0 | 0000 | xxxx | 0100 | 1 | 0 | 0 | 101 | xxxxxxxx |
| Rf5 = rf4 or rf2 | 0100 | 0010 | 0101 | 1 | 0 | 0 | 100 | xxxxxxxx |
| Rf6 = rf3 add rf5 | 0011 | 0101 | 0110 | 1 | 0 | 0 | 000 | xxxxxxxx |